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U.S. UTILITY Patent Application

PATENT NUMBER and
ISSUE DATE

| APPL NUM | FILING DATE | CLASS | SUBCLASS | GAU | EXAMINER |
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| 10007391 | 10/22/2001 | 714 | 733 | 2484 | <i>[Signature]</i> |

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NSC

KERVEROS

**CONTINUING DATA VERIFIED:

THIS APPLICATION IS A CON OF PCT/DE00/01052 04/05/2000

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** FOREIGN APPLICATIONS VERIFIED:

GERMANY 199 17 884.4 04/20/1999

PG-PUB DO NOT PUBLISH ☐

RESCIND ☐

Foreign priority claimed

☒ yes ☐ no

35 USC 119 conditions met

☒ yes ☐ no

Verified and Acknowledged Examiners's initials

ATTORNEY DOCKET NO

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TITLE : Circuit including a built-in self-test

U.S. DEPT. OF COMM./PAT. & TM.-PTO-436L (Rev. 12-94)

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| NOTICE OF ALLOWANCE MAILED | | Assistant Examiner | CLAIMS ALLOWED | |
|---|-----------|--------------------|----------------------|----------------------|
| | | | Total Claims | Print Claim for O.G. |
| ISSUE FEE | | Primary Examiner | DRAWING | |
| Amount Due | Date Paid | | Sheets Drwg. | Figs. Drwg. |
| <input type="checkbox"/> TERMINAL DISCLAIMER | | PREPARED FOR ISSUE | Application Examiner | |
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